Filed: 08-05-03 / Serial No.: 10/635,836
For: METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS



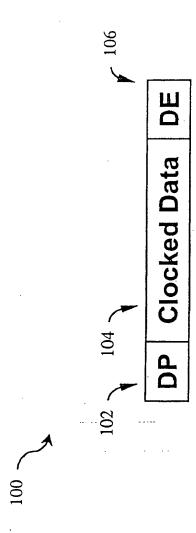


Fig.1 (Prior Art) Alpha Format Packet

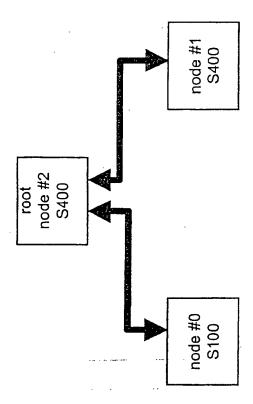
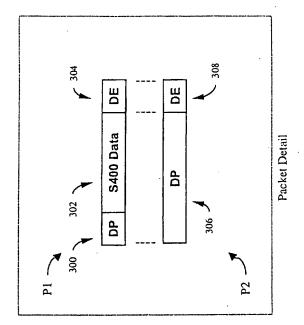


Fig. 2 (Prior Art) Legacy Compatibility Example

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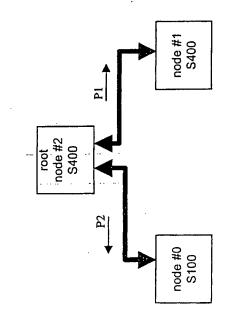


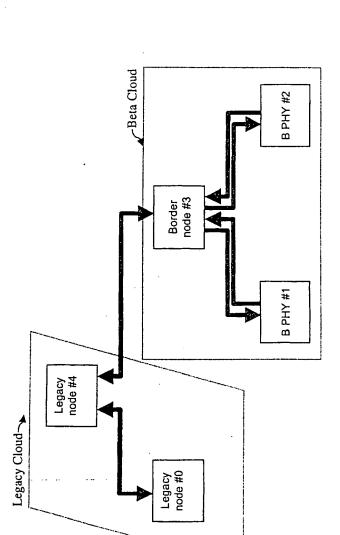
Fig. 3 (Prior Art) Legacy Speed Filtering Example

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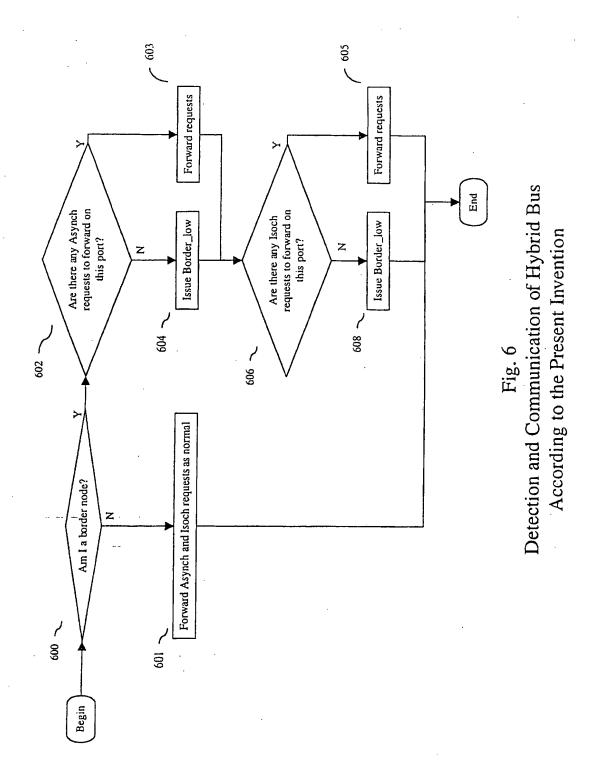
Fig. 4 (Prior Art) P1394b Packet Format



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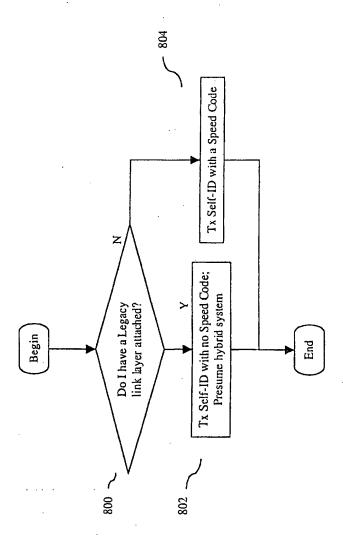
Data Communications System Having both P1394b and Legacy Devices Fig. 5 (Prior Art)



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REPLACEMENT SHEET --- Sheet 8 of 13

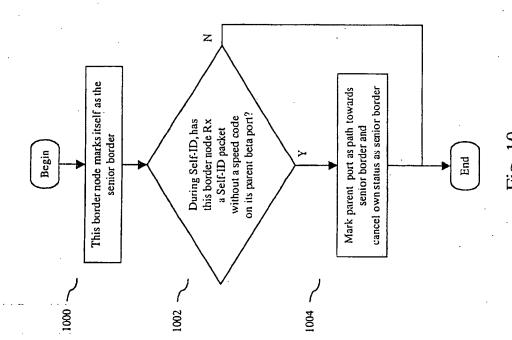


Detection and Communication of a Legacy Link] According to the Present Invention Fig. 8

REPLACEMENT SHEET --- Sheet 9 of 13

Rx Self-ID packet with no Speed Code? Presume hybrid system Begin 902

Detection and Communication of Hybrid Bus According to the Present Invention

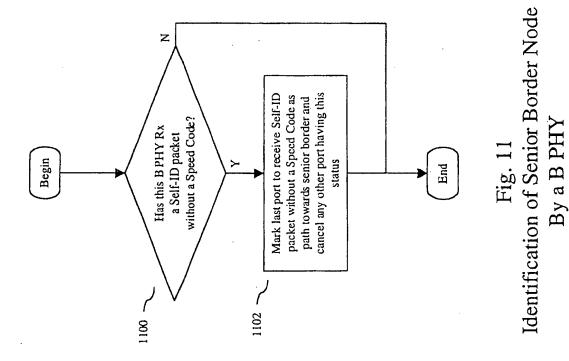


Detection of Path to Senior Border According to the Present Invention By a Border PHY Fig. 10

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REPLACEMENT SHEET --- Sheet 11 of 13

According to the Present Invention



For: METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS

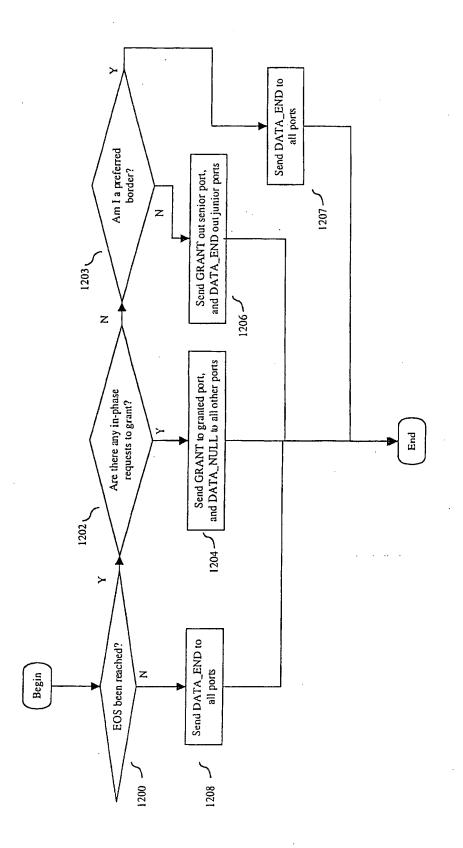


Fig. 12
Restoring Senior Border as BOSS
According to the Present Invention

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ARG

SAG

EOP

EOP

End of Isoch Packet

Hybrid Bus

B Bus

ARG ARG SAG SAG Fig. 13 OK_TO_GRANT Profile EOP EOP EOP ACK Missing EOP !EOS (After asynchronous packet) (After asynchronous packet)

According to the Present Invention